Application Note

How to bias the LL1667 / LL1668

DC Current (mA)					Max Signal (AC Volt, RMS)	Max Signal (AC Volt, RMS)
LL1667/5	**LL1667/7**	**LL1667/15**	**LL1667/20**	**LL1668/25**	LL1667	LL1668
5,0	7,0	15,0	20,0	25,0	390	235
5,3	7,4	15,8	21,0	26,2	359	216
5,5	7,8	16,6	22,2	27,5	327	196
5,8	8,2	17,5	23,3	28,8	293	176
6,1	8,6	18,4	24,5	30,2	257	154
6,5	9,0	19,4	25,8	31,6	220	131
6,8	9,5	20,4	27,2	33,1	180	107
7,1	10,0	21,4	28,6	34,7	139	82
7,5	10,5	22,6	30,1	36,4	95	56
7,9	11,1	23,8	31,7	38,2	49	29
8,3	11,7	25,0	33,3	40,0	3	2

These Plate chokes are specified at the maximum AC signal, at the rated DC current. However in many applications the maximum AC signal is not applied to the choke.

This means, you can allow a higher DC current. The limit for this is the DC saturation current, but the saturation current is only possible at very small signal.

The above table tells you what the maximum AC signal is, at a choosen DC current.

Additionally, there is a "Hidden" reserve of about 15%, which Lundahl keeps for the datatsheet safety. So the listed values here can be used with great confidence.